respective supply paths coupled to respective supply terminals of the plurality of power amplifiers;

a mode selector that selects one of the plurality of power amplifiers to operate as a polar amplifier based on a characteristic of the input signal relative to at least one threshold parameter;

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an output arbitrator that produces a unified output signal from respective outputs of the plurality of power amplifiers; and

a correction path that mitigates signal distortion and out-of-band (OOB) emissions associated with the unified output signal.

33. (New): The system of claim 32, the correction path comprising:

a digital-to-analog converter (DAC) having an input coupled to the mode selector;

a first summer having a first input coupled to an output of the DAC and a second input coupled to an output of the output arbitrator through a coupler;

a cancellation amplifier having an input coupled to an output of the first summer and an output coupled to a second summer, wherein the DAC receives a digital representation of a reference signal corresponding to a desired amplified output signal that is converted into an analog reference signal and combined with a portion of the unified output signal through the first summer to provide a cancellation signal that is amplified by the cancellation amplifier, and the amplified cancellation signal is inverted and combined with a delayed version of the unified output signal through the second summer.